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Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

New U.S. Patent Application
Title: SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD
Inventor and Address:

1) Osamu ARISUMI
Yokohama-shi, Japan

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. A check for \$768.00 representing the filing fee.
2. Application - 51 pages, including 4 independent claims and 20 claims total.
3. Drawings - 23 sheets of formal drawings containing 56 figures.
4. Information Disclosure Statement and Information Disclosure Citation, PTO 1449 with document attached.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f).
Applicant awaits notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicant claims the right to priority based on Japanese Patent Application No. 11-200901, filed on July 14, 1999.

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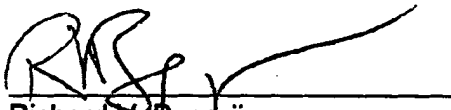
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Assistant Commissioner for Patents
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Please accord this application a serial number and filing date.

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or § 1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
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By: 
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RVB/FPD/sci
Enclosures

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